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Practitioner's Docket No. MI22-1171

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Akram et al., Salman

Application No.: 09/292,132

Filed: 04/14/99

For: Methods of Forming a Transistor Gate

Group No.: 2812

Examiner: S. Mulpuri

Commissioner for Patents  
Washington, D.C. 20231

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Examiner Interview Summary of February 2, 2005

February 3, 2005  
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Application Serial No. 09/292,132  
Examiner Interview Summary of 02/02/05

MI22-1171

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application Serial No. .... 09/292,132  
Filing Date .... April 14, 1999  
Inventor .... Salman Akram et al.  
Assignee .... Micron Technology, Inc.  
Group Art Unit .... 2812  
Examiner .... Savitri Mulpuri  
Attorney's Docket No. .... MI22-1171  
Title: Methods of Forming a Transistor Gate

**EXAMINER INTERVIEW SUMMARY OF FEBRUARY 2, 2005**

To: Commissioner for Patents  
Alexandria, VA 22313

**VIA FACSIMILE**

From: D. Brent Kenady  
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Wells St. John P.S.  
601 West First Avenue, Suite 1300  
Spokane, WA 99201-3828

Applicant's representative held multiple interviews with Examiner Mulpuri on February 2, 2005. Applicant's representative would like to thank Examiner Mulpuri for her time and attention to this matter.

Agreement was reached. The Examiner agreed that the deficiencies/inconsistencies in the Office Action warrant a new Office Action and a new time period for filing the response without extension fees. Accordingly,

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the Examiner agreed that: 1) no extension fees are due; 2) a new time period for responding begins from February 2, 2005; 3) and that one month is allowed from February 2, 2005 to respond to the new office action without extension fees. Therefore, the new due date for responding to the new office action without extension fees is March 2, 2005.

Applicant' representative would again like to thank Examiner Mulpuri for her time and attention to this matter.

Respectfully submitted,

Dated: 2-3-05

By:   
D. Brent Kenady  
Reg. No. 40,045